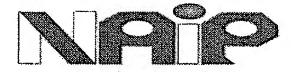
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Customer No.: 27765

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Art Unit: 2814

Fax: (571) 273-8300

From: Winston Hsu, Registration No. 41,526

Serial No.: 10/791,709

Attorney Docket No.: UECP0012USA

Subject: Information Disclosure Statement (IDS)

Total Pages: 46 pages (including cover page)

Winston Hsu 2005/11/03

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This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. This will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS, SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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FEE TRANSMITTAL					Filing Dat	8	03/04/2004			
For FY 2005					First Nam	ed Inventor	Jen-Chau Wu			
<u> </u>					Examiner	Name	PHAM, LONG			
Applicant of	laims small ent	ily status	. See 37 CFR 1.2	Art Unit		2814				
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Complete if Known Substitute for form 1449/PTO Application Number 10/791,709 Filing Date 03/04/2004 INFORMATION DISCLOSURE First Named Inventor Jen-Chau Wu STATEMENT BY APPLICANT Art Unit 2814 (Use as many sheets as necessary) Examiner Name PHAM, LONG UECP0012USA Sheet 1 Attorney Docket Number

Examiner	Cho	Dogument Number	U. S. PATENT D Publication Date	Name of Patentee or	Onesa Calumna Illan Mil		
Initials*	7 Cite Document Number No.1 Number-Kind Code ^{2 (A Arcons)}		MM-DD-YYYY	Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
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		FOREIG	N PATENT DOCU	MENTS		
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		Country Code ³ Number ⁴ Kind Code ⁵ (if known)	MM-DD-YYYY		Or Relevant Figures Appear	Τ*
	1	JP P2001-15817A	01-19-2001		Para 0044, 0045, Flys. 1 and 2	+
	2	JP P2001-93320A	04-06-2001		Para 0023, Figs. 2 and 3	+
	3	JP 8-56034	02-27-1996	:	Para 0012-0014, Fig. 11	+
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered, include copy of this form with next communication to applicant, 'Applicant's unique citation designation number (optional). *See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. *Senter Office that Issued the document, by the two-letter code (WIPO Standard ST.3), *For Japanese patent documents, the Indication of the year of the reign of the Emperor must precade the serial number of the patent document. *Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.18 if possible. *Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Wu, Jen-Chau et al.

5 Appl. No.: 10/791,709 Filing Date: 03/04/2004

Examiner: Pham, Long Art Unit: 2814

Docket No.: UECP0012USA Confirmation No.: 5086

Title: ESD PROTECTION CONFIGURATION AND METHOD FOR LIGHT

10 EMITTING DIODES

To: Commissioner for Patents

P.O. BOX 1450

Alexandria, VA 22313-1450

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Subject: Information disclosure statement under 37 CFR \$1.56

Dear Sir,

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This is an Information Disclosure Statement in accordance with the duty to disclose information material to patentability under 37 CFR \$1.56. The applicant wishes to make of record the documents listed on the accompanying form PTO/SB/08.

Since this IDS is filed after the mailing date of the first Office action but before notice of allowance, consideration of the information disclosure statement is hereby requested according to 37 CFR \$1.97(c).

That each item of information contained in the information

From: 8064986673 To: 00215712738300 Page: 6/46 Date: 2005/11/3 下午 03:09:15

disclosure statement was first cited in an Office communication mailed on September 6, 2005 for the counterpart Japan patent application number 2003-318638, which are no more than three months from the filing of the information disclosure statement.

According to the requirement set forth in 37 CFR \$1.98, a copy of each cited reference and its English language abstract is included to fulfill the concise explanation requirement. The title of the cited references, Japanese patent application number JP P2001-15817A, Japanese patent application number JP P2001-93320A, and Japanese Patent number JP 8-56034 and an English language abstract of each cited reference are as follows.

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For cited reference JP P2001-15817A:

Title:

COMPOUND LIGHT-EMITTING ELEMENT, LIGHT-EMITTING DIODE AND 20 MANUFACTURE THEREOF

Abstract:

PROBLEM TO BE SOLVED: To provide a compound light-emitting element of a constitution, wherein the layer thickness of a resin containing a fluorescent material, for making a wavelength conversion of light from the main light-extraction surface of a flip-chip light-emitting element into a wavelength conversion of white light is optimized and the pure white light is obtained, and to provide a light-emitting device and the manufacturing method of the device.

SOLUTION: A flip-chip light-emitting element 1 conductively mounted on a submount element 2 is provided, the

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periphery of the element 1 is covered with a first resin 16 containing a fluorescent material for making a wavelength conversion of light from this element 1 using the element 2 as a saucer, either of the main light- extraction surface of the upper surface of a transparent substrate 1a of the element 1 and the shell surface (top panel) of the resin 16 or both of the main light-extraction surface and the shell surface (top panel) is or are provided in parallel to the rear electrode formation surface of the element2, the film thickness of the resin 16 on the main light-extraction surface is uniformly formed, and light from the entire main light-extraction surface of the element 1 is evenly made a wavelength conversion to enable an emission having not a color shading.

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For cited reference JP P2001-93320A:

Title:

SURFACE LIGHT SOURCE UNIT

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Abstract:

PROBLEM TO BE SOLVED: To improve the constraint of a combination of an edge line type surface light source unit integrated with a LED as a light source and a mother substrate and provide more resistance to static electricity of the LED.

SOLUTION: An edge light type surface light source unit 10 comprises a light guide member 1 having at least one through-hole 8 or blind hole for refracting or reflecting an emitted light from a light source and reflecting parts 6 provided at both sides of a portion opposed to a LED 2 on the side face of the light guide member. The LED 2 is connected to a capacitor (not illustrated) for preventing its

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destruction and an adjusting resistance and mounted on a LED substrate 13 and the LED substrate 13 is joined to a side face 12c opposed to the through-hole 8 or the blind hole in the light guide member

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For cited reference JP 8-56034:

Title:

10 SEMICONDUCTOR LIGHT EMITTING ELEMENT DRIVE CIRCUIT

Abstract:

PURPOSE: To obtain a current pulse waveform free of overshoot and ringing, and prevent generation of parasitic inductance.

CONSTITUTION: A semiconductor light emitting element drive circuit for driving a light emitting element 4 is provided with the following a bipolar transistor 1 which drives the light emitting element 4 by applying a control signal for driving the light emitting element 4 to the base, and making a current flow across the emitter and the collector in response with the control signal, and an insulated gate type transistor 3 which acts as a constant current supply connected with the bipolar transistor 1.

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It is respectfully requested that the examiner can consider the documents listed on the accompanying form PTO/SB/08 and that it be made of record in the application. The applicants sincerely hope that the examiner initials the cited references on the form and that a copy of the initialed form be sent to the applicants with the next communication from the examiner.

From: 8064986673 To: 00215712738300 Page: 9/46 Date: 2005/11/3 下午 03:09:16

Respectfully submitted,

Winten the

Date: November 3, 2005

Winston Hsu, Patent Agent No. 41,526

5 P.O. BOX 506, Merrifield, VA 22116, U.S.A.

Voice Mail: 302-729-1562 Facsimile: 806-498-6673

e-mail: winstonhsu@naipo.com

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